## Features

- 2.0-20.0 GHz Operation
- 3 dB Insertion Loss
- TTL Control
- Excellent Match on Off Port
- MSAG ${ }^{\text {TM }}$ Process


## Description

The MASWGM0002-Die is a single pole double throw switch that is fully matched to 50 ohms on both the input and output.


Fabricated using M/A-COM's repeatable, high performance and highly reliable GaAs Multifunction Self-Aligned Gate (MSAG ${ }^{\text {TM }}$ ) Process, each device is $100 \%$ RF tested on wafer to ensure performance compliance.

M/A-COM's MSAG ${ }^{\text {TM }}$ process features robust silicon-like manufacturing processes, planar processing of ion implanted transistors, multiple implant capability enabling power, lownoise, switch and digital FETs on a single chip, and polyimide scratch protection for ease of use with automated manufacturing processes. The use of refractory metals and the absence of platinum in the gate metal formulation prevents hydrogen poisoning when employed in hermetic packaging.

## Primary Applications

- Test Equipment and Instrumentation
- Electronic Warfare
- Weather and Military Radar
- Point to Point Communications
- VSAT

Electrical Characteristics: $\mathrm{T}_{\mathrm{B}}=25^{\circ} \mathrm{C}^{1}, \mathrm{Z}_{0}=50 \Omega, \mathrm{~V}_{\mathrm{EE}}=-5 \mathrm{~V}$

| Parameter | Symbol | Typical | Units |
| :---: | :---: | :---: | :---: |
| Bandwidth | f | $2.0-20.0$ | GHz |
| Insertion Loss @ 10 GHz | IL | 3 | dB |
| Insertion Loss @ 20 GHz | IL | 4.3 | dB |
| Isolation @ 10 GHz | ISO | 50 | dB |
| Isolation @ 20 GHz | ISO | 35 | dB |
| Input VSWR (On) | VSWR | $1.4: 1$ |  |
| Output VSWR (On) | VSWR | $1.4: 1$ |  |
| Output VSWR (Off) | VSWR | $1.2: 1$ | dBm |
| Input Third Order Intercept | ITOI | 29 | dBm |
| Input 1-dB Compression Point | P1dB | 26 |  |

1. $\mathrm{T}_{\mathrm{B}}=$ MMIC Base Temperature

## Maximum Operating Conditions ${ }^{2}$

| Parameter | Symbol | Absolute Maximum | Units |
| :---: | :---: | :---: | :---: |
| Input Power | $\mathrm{P}_{\mathrm{IN}}$ | 31 | dBm |
| Digital Driver Voltage | $\mathrm{V}_{\mathrm{EE}}$ | -6.0 | V |
| Junction Temperature | $\mathrm{T}_{\mathrm{J}}$ | 180 | ${ }^{\circ} \mathrm{C}$ |
| Storage Temperature | $\mathrm{T}_{\text {STG }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |

2. Operation outside of these ranges may reduce product reliability. Operation at other than the typical values may result in performance outside the guaranteed limits.

## Recommended Operating Conditions

| Characteristic | Symbol | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Digital Driver Voltage | $\mathrm{V}_{\mathrm{EE}}$ | -5.2 | -5 | -4.8 | V |
| Digital Driver Current | $\mathrm{I}_{\mathrm{EE}}$ | 3 | 5 | 10 | mA |

TTL Path Selection ${ }^{3}$

| Characteristic | Logic Level <br> (@ Pad A) | Min | Typ | Max | Unit |
| :---: | :---: | :---: | :---: | :---: | :---: |
| Com-Out1 | Logic High | 3.0 | 5.0 | 5.0 | V |
| Com-Out2 | Logic Low | 0 | 0 | 0.4 | V |

3. Voltage Levels valid from $-50^{\circ} \mathrm{C}$ to $+150^{\circ}$ Base Temperature for $\mathrm{V}_{\mathrm{EE}}=-5 \mathrm{~V}$.

## Operating Instructions

This device is static and light sensitive. Digital circuitry operation can be impaired under high intensity light, e.g. microscope light. Please handle with care. To operate the device, follow these steps.

1. Power Up: Apply $\mathrm{V}_{\mathrm{EE}}=-5 \mathrm{~V}$.
2. Apply Logic Voltages to control Circuits as listed in Recommended Operating Conditions
3. Power Down: Set $\mathrm{V}_{\mathrm{EE}}=0$


- North America Tel: 800.366.2266 / Fax: 978.366.2266
- Europe Tel: 44.1908.574.200 / Fax: 44.1908.574.300
- Asia/Pacific Tel: 81.44.844.8296 / Fax: 81.44.844.8298

Visit www.macom.com for additional data sheets and product information.

Switch, Single-Pole, Double-Throw


Figure 1. Input and Output VSWR vs. Frequency


Figure 2. Insertion Loss and Isolation vs. Frequency

Switch, Single-Pole, Double-Throw
2.0-20.0 GHz

Mechanical Information
Chip Size: $2.054 \times 1.284 \times 0.075 \mathrm{~mm} \quad(81 \times 51 \times 3 \mathrm{mils})$


Figure . Die Layout

## Bond Pad Dimensions

| Pad | Size $(\mu \mathrm{m})$ | Size (mils) |
| :---: | :---: | :---: |
| RF: COMMON, OUT1, OUT2 | $125 \times 125$ | $5 \times 5$ |
| Digital Driver Voltage $\mathrm{V}_{\text {EE }}$ | $125 \times 125$ | $5 \times 5$ |
| A (TTL Control) | $125 \times 125$ | $5 \times 5$ |

Switch, Single-Pole, Double-Throw
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## Assembly and Bonding Diagram



Figure 4. Recommended bonding diagram for pedestal mount.
Support circuitry typical of MMIC characterization.

## Assembly Instructions:

Die attach: Low thermal conductivity silver epoxies are acceptable for die attach of this MMIC. Follow the manufacturer's instructions. If solder is employed, use $\mathrm{AuSn}(80 / 20) 1$ mil preform solder. Limit time @ $300^{\circ} \mathrm{C}$ to less than 5 minutes.

Wirebonding: Bond @ $160^{\circ} \mathrm{C}$ using standard ball or thermal compression wedge bond techniques. For DC and RF pad connections, use either ball or wedge bonds. For best performance, especially above 10 GHz , wedge bonds of shortest length employed on the RF interconnects is preferred over ball bonds.

